

AMENDMENTS TO THE DRAWINGS

The attached sheet(s) of drawings includes change to FIG. 3A and 3B.

Attachment: Replacement sheet
 Annotated sheet showing changes

REMARKS

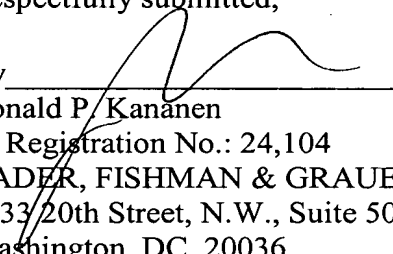
This Preliminary Amendment is requested prior to the initial examination of the above-identified patent application to correct FIGS. 3A and 3B.

No new matter has been added.

If the Examiner has any suggestions for placing this application in even better form, the Examiner is invited to telephone the undersigned and the number listed below.

Dated: February 10, 2006

Respectfully submitted,

By  _____

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ANNOTATED SHEET SHOWING CHANGES

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FIG. 3

FIG. 3 A

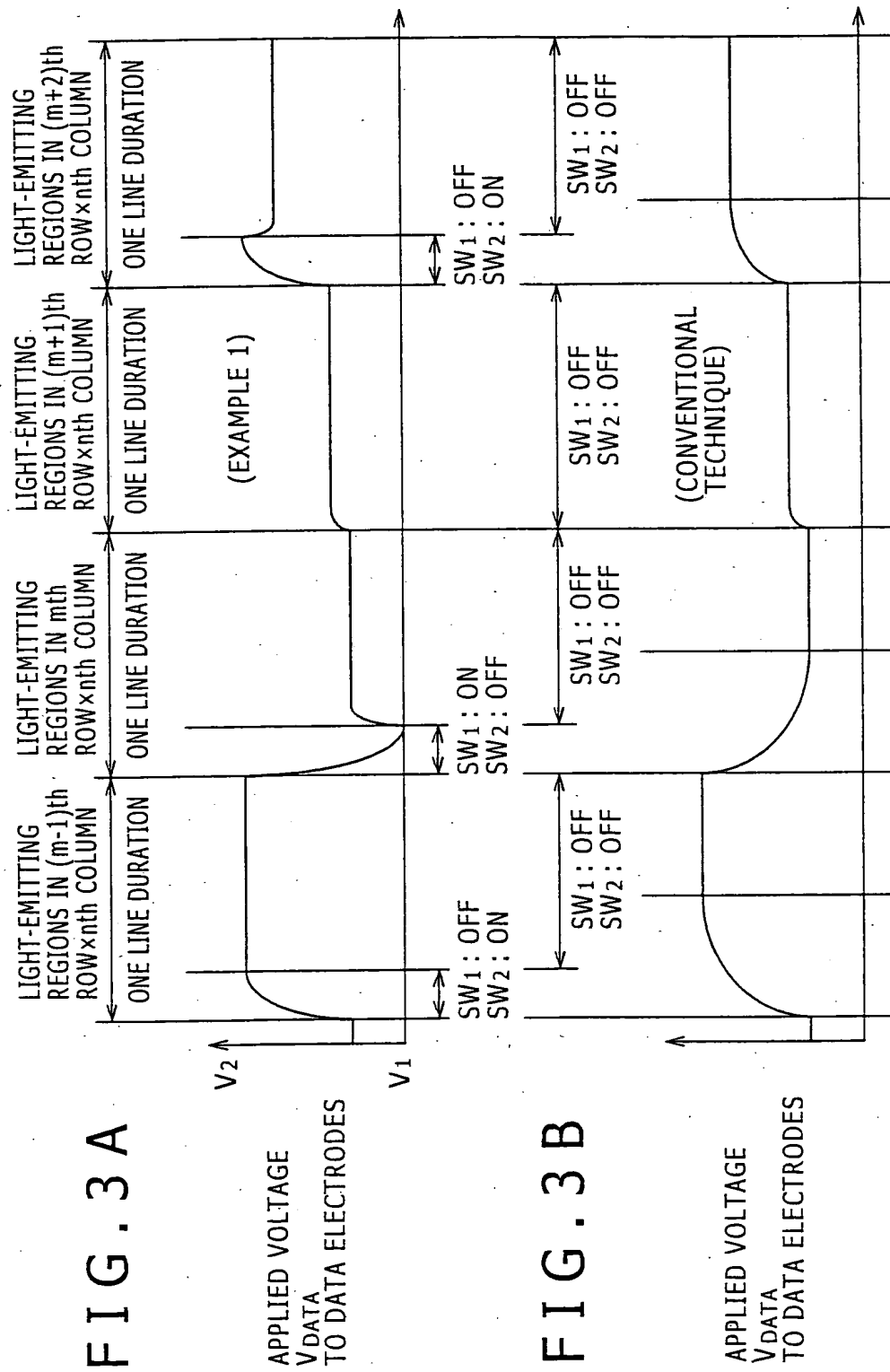


FIG. 3 B

APPLIED VOLTAGE V_{DATA} TO DATA ELECTRODES

FIG. 3 B illustrates the timing of data voltage application to data electrodes for four light-emitting regions in the m th row of an n th column. The regions are labeled: LIGHT-EMITTING REGIONS IN $(m-1)$ th ROW x th COLUMN, LIGHT-EMITTING REGIONS IN m th ROW x th COLUMN, LIGHT-EMITTING REGIONS IN $(m+1)$ th ROW x th COLUMN, and LIGHT-EMITTING REGIONS IN $(m+2)$ th ROW x th COLUMN. Each region is associated with a "ONE LINE DURATION" period.

The timing diagram shows the applied voltage V_{DATA} (labeled V_1 and V_2) and the states of two switches, SW_1 and SW_2 , for each region. The states are:

- Region 1: SW_1 : OFF, SW_2 : OFF
- Region 2: SW_1 : OFF, SW_2 : OFF
- Region 3: SW_1 : OFF, SW_2 : OFF
- Region 4: SW_1 : OFF, SW_2 : OFF

(CONVENTIONAL TECHNIQUE)